

Figure 1

# DIGITAL SIGNAL PROCESOR 11

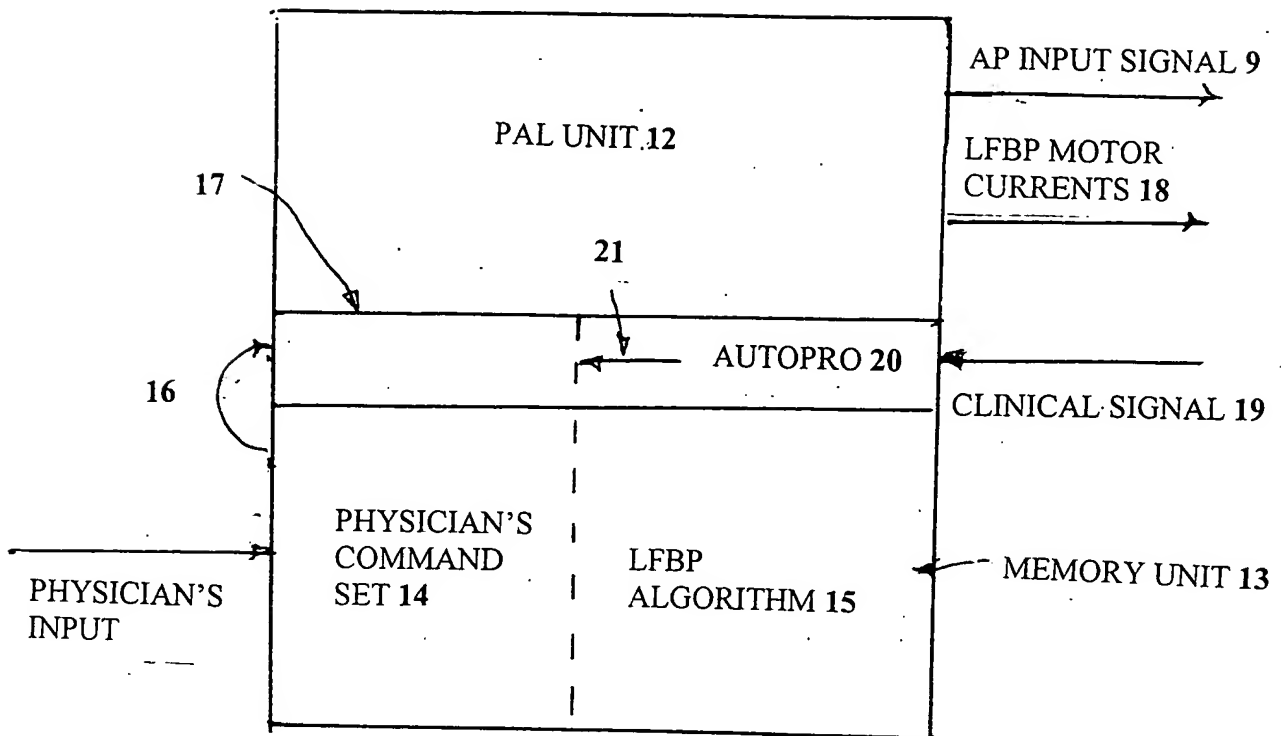


Figure 2.

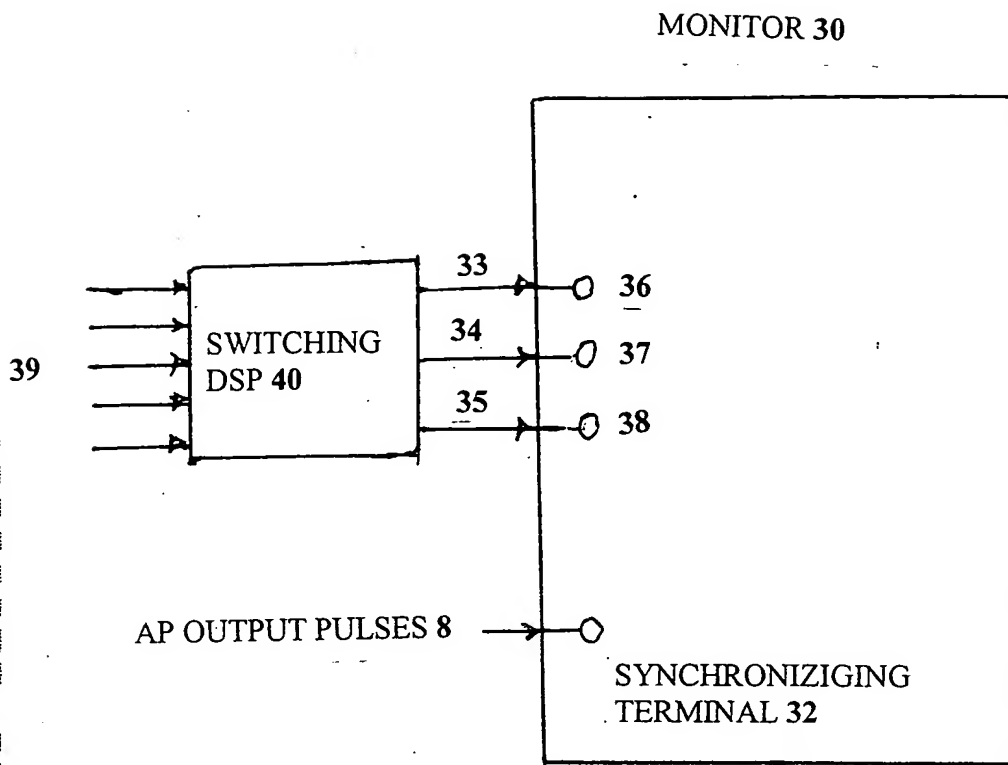


Figure 3

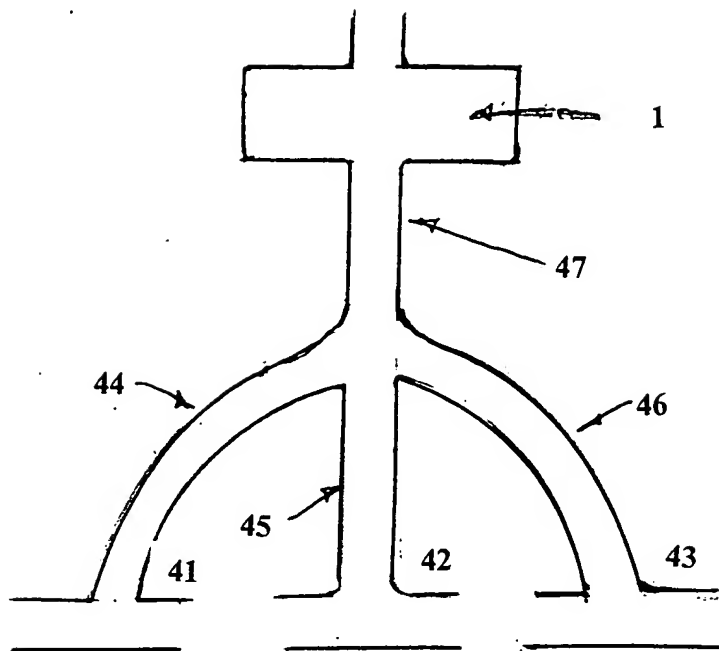


Figure 4

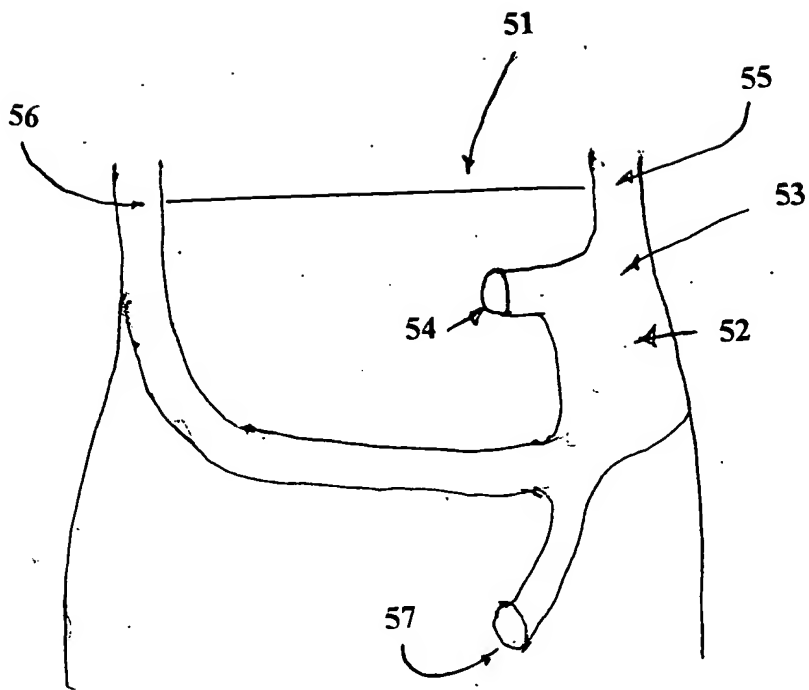


Figure 5

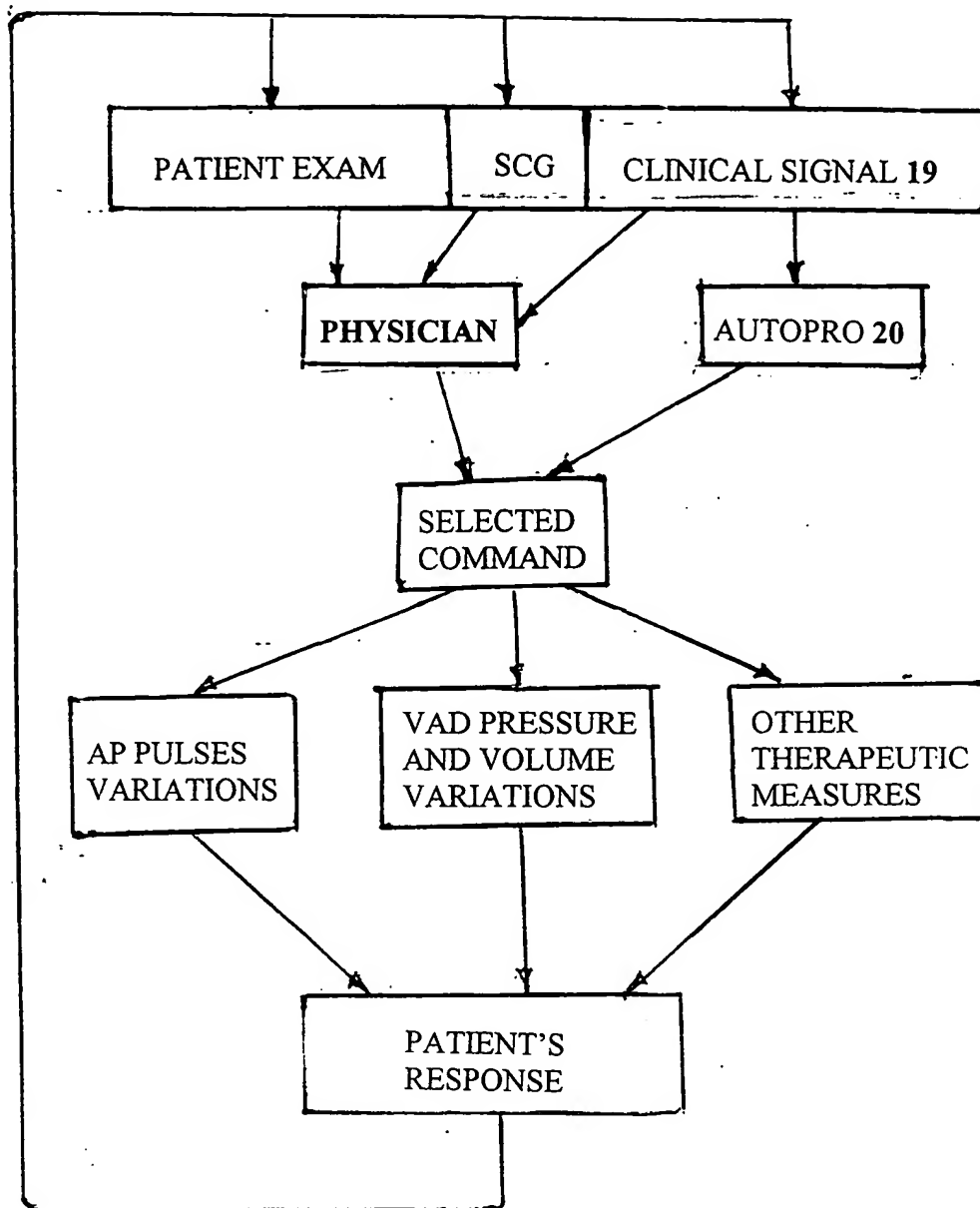


Figure 6

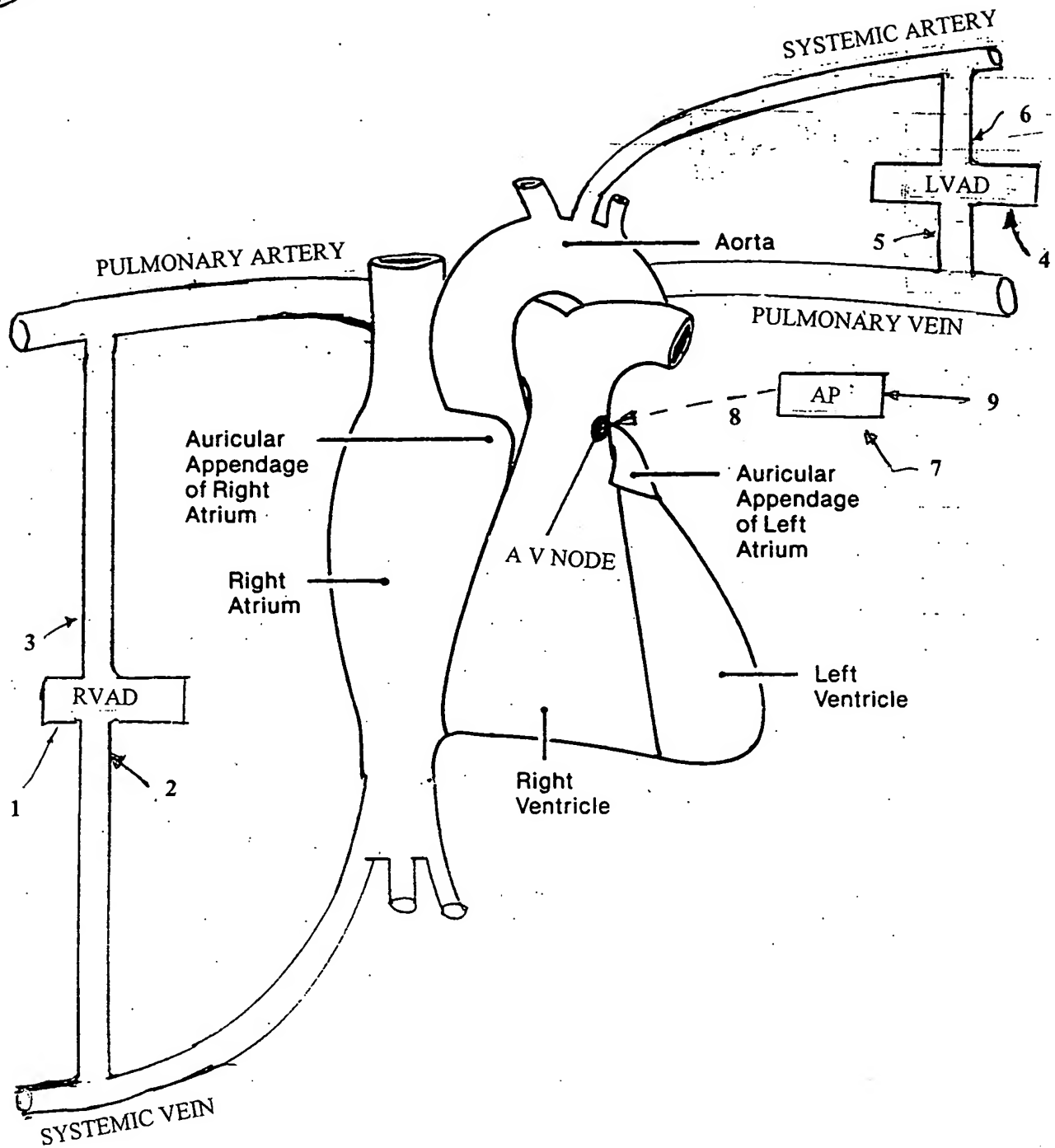


Fig. 1



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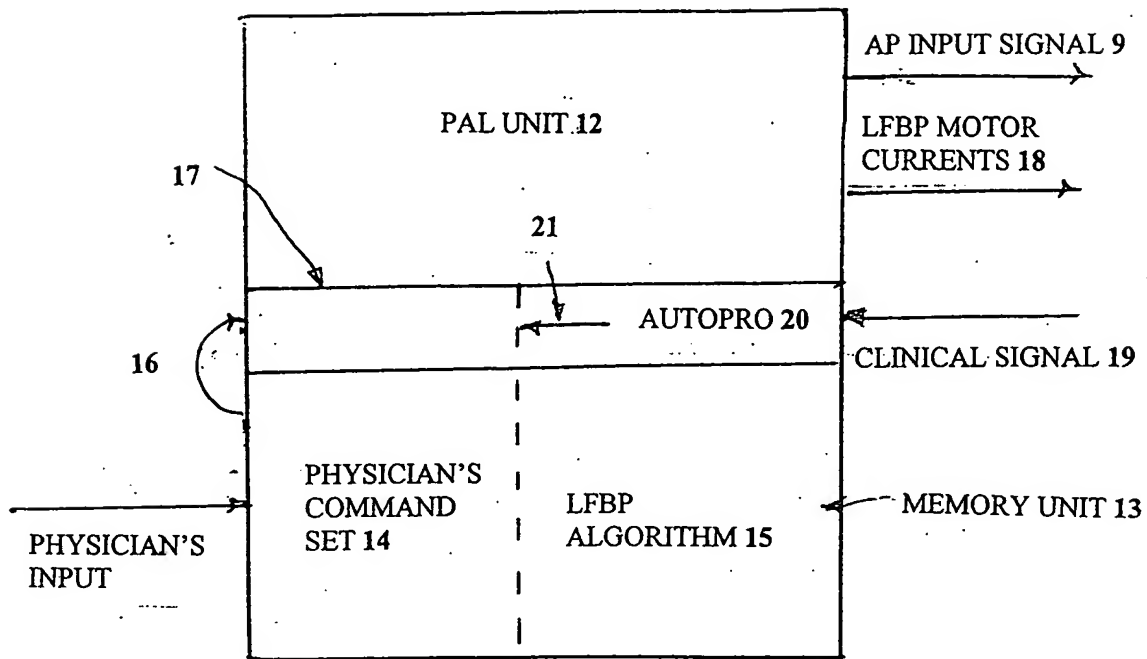




Fig. 3

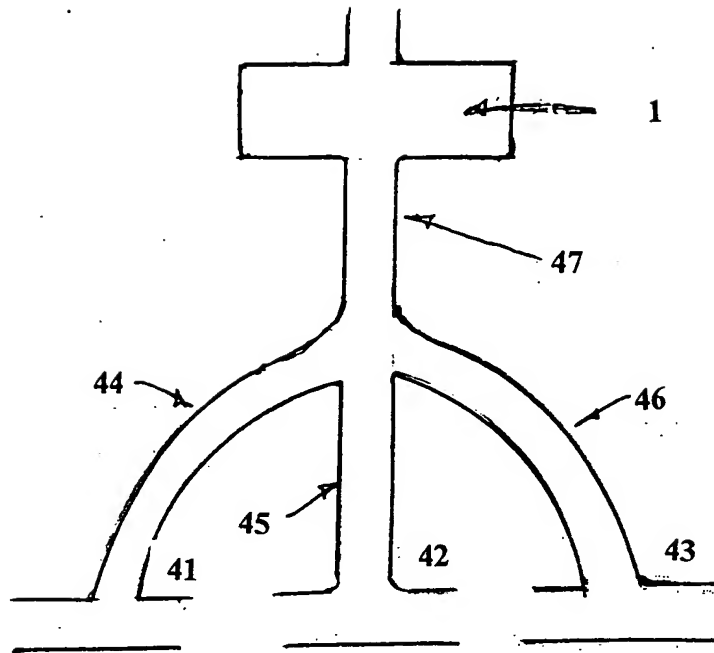


Fig. 4

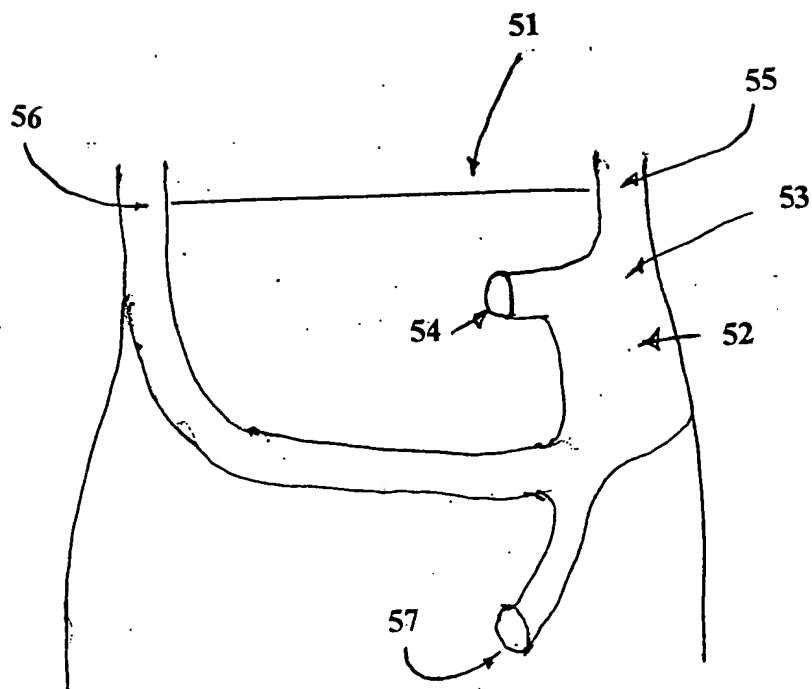


Fig. 5

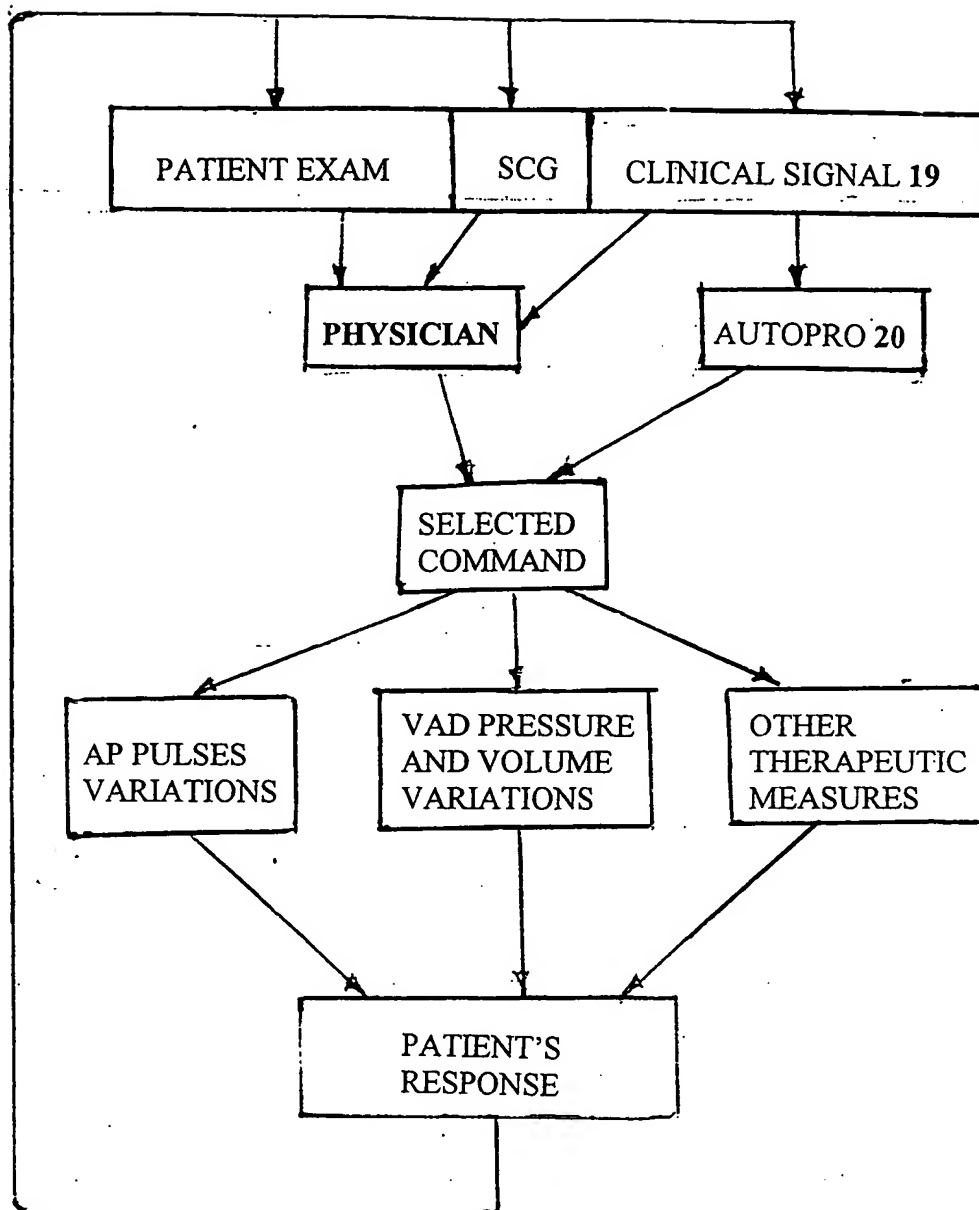


Fig. 6